

Attorney Docket No. 110348-134848  
IPN P17805

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In Application of:

Dubin et al.

Application No.: 10/763,470

Filed: 01/22/2004

For: ELECTROLESS PLATING  
SYSTEMS AND METHODS

Examiner: Lamb, Brenda A.

Art Unit: 1734

Confirmation No.: 6669

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

**Supplemental Declaration of Inventor Pursuant to 37 C.F.R. § 1.112**

Madam:

We, Valery M. Dubin and Chin-Chang Cheng, hereby declare that:

1. At the time of the invention, we were citizens of the United States of America and the Republic of China (Taiwan), respectively, and were residents of the State of Oregon, residing in or around the greater Portland metropolitan area.
2. We are the joint inventors of the subject matter of pending independent claim 5, and claims 6-8 through their dependencies to claim 5, of the above-captioned application, which was filed jointly by ourselves and Arnel Fajardo, on or around January 22, 2004.
3. At the time the subject matter of claim 5 was conceived we were employed by Intel Corporation of Santa Clara, California, the assignee.

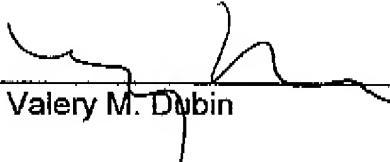
Attorney Reference No. 110348-134848  
IPN P17805

Patent

4. We further declare that we conceived the subject matter of the above-captioned application as embodied in pending claim 5, and claims 6-8 through their dependencies to claim 5, which was described but not claimed in earlier filed U.S. Patent Application 10/046,218 ('218). '218 was filed jointly by us, Vincent F. Caillouette, and Christopher D. Thomas on January 16, 2002.

**We further declare that all statements made herein of our own individual knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the above-identified patent application or any patent issued thereon.**

Executed by and on the date as set forth below:

By:   
Valery M. Dubin

Date: 26 December 2007

By: \_\_\_\_\_  
Chin-Chang Cheng

Date: \_\_\_\_\_

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**Supplemental Declaration of Inventor Pursuant to 37 C.F.R. § 1.132**

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Executed by and on the date as set forth below:

By: \_\_\_\_\_  
Valery M. Dubin

Date: \_\_\_\_\_

By:   
Chin-Chang Cheng

Date: 07/08/2008